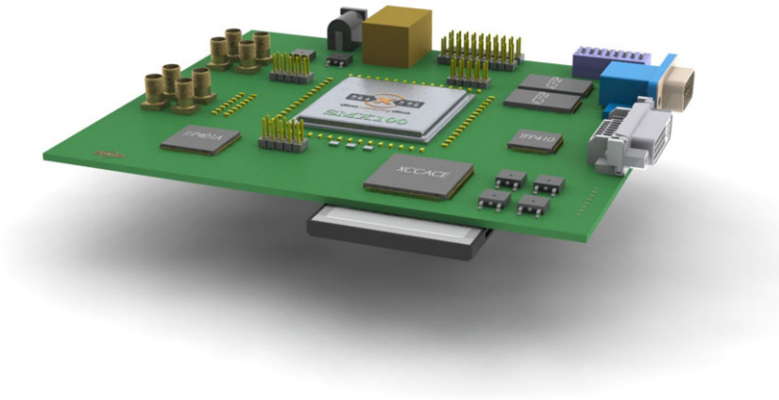
silicon connecting silicon



SXM100DB

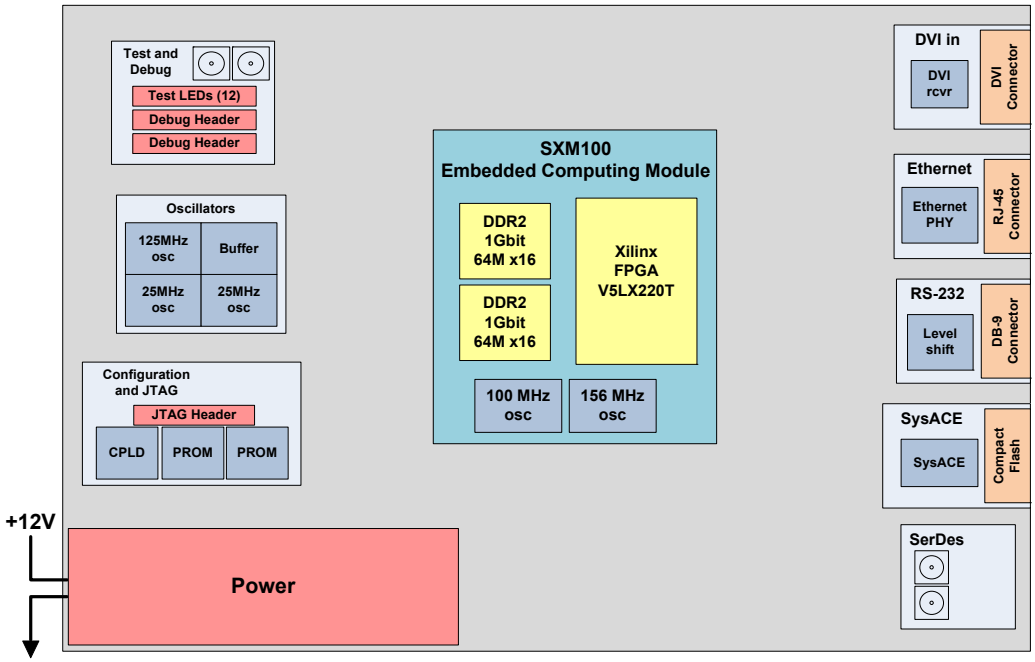
Development Board

EVALUATION TOOL FOR SXM100 EMBEDDED COMPUTING MODULE

The SXM100DB is an evaluation board for the SXM100 embedded computing module. The board supports industry standard interfaces for networking, video and system configuration to support evaluation of the SXM100 module. Bitstreams supporting Linux, a web server and simple image generation are supported.

Key Features

- SXM100 Embedded Computing Module
- JTAG interface
- DVI output
- 10/100/1000 Ethernet
- RS-232 serial port
- Compact Flash
- 2 SerDes lanes at 3.125 GHz each



SXM100DB Development Board

SXM100DB Technical Specifications

SXM100 Embedded Computing Module:

- Xilinx XC5VLX220T
  - 138,240 FFs
  - 7,632 kbits embedded memory
  - 128 18-bit multipliers
- DDR2 Memory
  - 2 each 64M x 16 bits
  - Independent access

Interfaces:

- DVI output
- 10/100/1000 Ethernet
- JTAG
- RS232
- Compact Flash
- SMA connector for SerDes outputs

Demonstration Bitstreams:

- Microblaze
  - Pico Linux
  - Web server
  - Boots from compact flash
- Graphics
  - spinning logo
  - fractal display

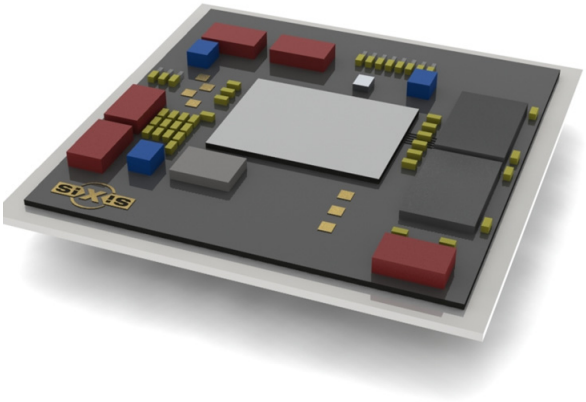
Mechanical:

- 200 x 300 x 20 mm

Required Input Power:

- 12 VDC

SXM100 Embedded Computing Module



SXM100 Embedded Computing Module with lid removed

The SXM100 is an off-the-shelf high performance embedded computing module integrating a Xilinx Virtex-5 FPGA, and DDR2 memories with interconnect and power distribution. The module utilizes siXis' unique Silicon Circuit Board (SiCB) technology which combines bare-die and packaged components on large area silicon substrates. This provides superior size, weight, and power characteristics in an industry-standard package allowing it to be processed using commercial methods.

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